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ProbFOR gate is fault-freeg ProbAt least 1 of the AND  
gates is stuck-at-1g =  $p_1 + (1-p_0-p_1)[1-(1-p_1)^3]$ . The  
probability that the output of the voter will be stuck-  
at-0 is ProbX is stuck-at-0 j the inputs are correctg =  
ProbFOR gate stuck -at-0g+ ProbFOR gate is fault-  
freeg ProbAll AND gates are stuck-at-0g =  $p_0$   
 $+ (1-p_0-p_1)p_3$  0. 11.

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A fault-tolerant system that processes transactions for a financial services firm uses three separate computers. If the operating computer fails, one of the two spares can be immediately switched online. After the second computer fails, the last computer can be immediately switched online. Assume that the probability of a failure during any transaction is  $10^{-8}$  and that the transactions can be considered to be independent events.

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1. A technique for fault-tolerant software, suggested by Randell [RAND 1978), consists of a primary and

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an alternate module for each critical task, together with a test for determining whether a module performed its function correctly. Such a construct is called a recovery block. Define the following events: A = "primary module functions correctly."

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